

EAST Search History

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L40	0	(signals AND pseudo-randomly generated bits AND selected bit line AND non-selected bit lines AND bit lines AND bus AND memory controller AND serial presence detect circuit).clm.	USPAT; UPAD	ADJ	ON	2009/10/27 16:33
L41	0	(processor AND memory AND bus AND signals AND pseudo-randomly generated bits AND bit lines AND selected bit line AND data write/read operation).clm.	USPAT; UPAD	ADJ	ON	2009/10/27 16:35

10/27/09 5:08:47 PM

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\10766611.wsp